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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hajime YAMAMOTO et al.**

Art Unit: **1756**

Application Number: **10/647,247**

Examiner: **Daborah Chacko Davis**

Filed: **August 26, 2003**

Confirmation Number: **1773**

For: **METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE,
AND METHOD OF FORMING RESIST PATTERN**

Attorney Docket Number: **031029**
Customer Number: **38834**

RESPONSE UNDER 37 C.F.R. § 1.111

Mail Stop: Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

September 14, 2007

Sir:

This paper is filed in response to the Office Action dated June 15, 2007.

Remarks begin on page 2 of this paper.